

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-11811 US		10/027708	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					James Holt, John So			
					Filing Date		Group	
					December 19, 2001		Unknown	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
Cal	CA	4,573,099	2/25/1986	Ganesan et al.	361	56		
Cal	CB	6,100,677	8/8/2000	Farrenkopf	323	285		
Cal	CC	5,381,061	1/10/1995	Davis	326	57		
Cal	CD	5,319,259	6/7/1994	Merrill	307	443		
Cal	CE	5,063,304	11/5/1991	Iyengar	307	296.6		
Cal	CF	5,161,183	11/3/1992	Maytum	379	412		
Cal	CG	5,479,119	12/26/1995	Tice et al.	327	74		
Cal	CH	5,815,359	9/29/1998	Maytum et al.	361	111		
Cal	CI	5,581,170	12/3/1996	Mammanno et al.	320	17		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	CJ							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
Cal	CK	Analog Devices, "IMVP-II-Compliant Core Power Controller for Mobile CPUs," 2001, ADP3422.						
Cal	CL	Maxim, "Notebook CPU Step-Down Controller for Intel Mobile Voltage Positioning (IMVP-II)," www.maxim-ic.com , February, 2001, MAX1718.						
Cal	CM	Semtech, "Portable IMVP-IV™ Dual Phase Power Supply Controller," September, 2001, SC1476.						
Cal	CN	Texas Instruments, "High-Density Synchronous Buck Converter Design Using TPS56xx Controllers", User's Guide, June 1999.						
Cal	CO	Harris Corporation, "Advanced PWM and Dual Linear Power Control with Integrated ACPI Support Interface," December 1998, HIP6028.						
Cal	CP	Di Bernardo et al., "Grazing, skipping and sliding: analysis of the non-smooth dynamics of the DC/DC buck converter," September 22, 1997, journal of publication unknown.						
Examiner		Gary L. Laxton		Date Considered		6-27-03		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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	AA	5,091,818	2/25/1992	Morikawa et al.	361	56		
	AB	5,654,858	8/5/1997	Martin et al.	361	56		
	AC	5,784,231	7/21/1998	Majid et al.	361	18		
	AD	5,781,390	7/14/1998	Notaro et al.	361	84		
	AE	5,963,407	10/5/1999	Fragapane et al.	361	42		
	AF	5,818,120	10/6/1998	Palara	307	10.6		
	AG	5,285,344	2/8/1994	Heitzmann	361	22		
	AH	5,088,018	2/11/1992	Lee	363	56		
	AI	5,818,670	10/6/1998	Ahn	361	18		
	AJ	5,771,162	6/23/1998	Kwon	363	56		
	AK	5,764,467	6/9/1998	Seo	361	91		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	Intersil, "Microprocessor CORE Voltage Regulator Multi-Phase Buck PWM Controller," 3/2000, file 4765.1.						
	AR	National Semiconductor, "LM2633, Advanced Two-Phase Synchronous Triple Regulator Controller for Notebook," www.national.com/pdf/LM/LM2633.html , 2002.						
	AS	Linear Technology, "Dual High Efficiency, Low Noise, Synchronous Step-Down Switching Regulators," 1996, LTC 1538-AUX/LTC 1539.						
Examiner <i>Gary L. Loxton</i>		Date Considered <i>6-27-03</i>						
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CL	BA	5,751,531	5/12/1998	Rault	361	56		
CL	BB	5,930,096	7/27/1999	Kim	361	91		
CL	BC	6,052,022	4/18/2000	Lee	327	589		
CL	BD	5,130,883	7/14/1992	Edwards	361	91		
CL	BE	5,111,353	5/5/1992	Kotowski et al.	361	91		
CL	BF	5,359,281	10/25/1994	Barrow et al.	323	284		
CL	BG	6,061,218	5/9/2000	Ida et al.	361	56		
CL	BH	4,879,625	11/7/1989	Potenzzone	361	90		
CL	BI	5,164,874	11/17/1992	Okano et al.	361	56		
CL	BJ	5,483,406	1/9/1996	Bennett et al.	361	56		
CL	BK	5,335,132	8/2/1994	DeShazo, Jr.	361	18		
Foreign Patent Documents								
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	BL							
	BM							
	BN							
	BO							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
CL	BP	Linear Technology, "Wide Operating Range, No Rsense™ Step-Down Contoller," 2001, LTC1778/LTC1778-1.						
CL	BQ	Analog Devices, "5-Bit Programmable 2-Phase Synchronous Buck Controller," 2001, ADP3160.						
CL	BR	Analog Devices, "5-Bit Programmable Dual Power Supply Controller for Pentium® III Processors," 1999, ADP3154.						
Examiner		Date Considered						
Cary L. Carlton		6-27-03						
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